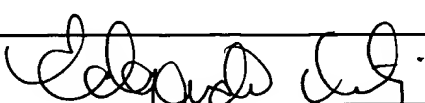


<div style="display: flex; align-items: center; justify-content: center;"> <div style="border: 2px solid black; border-radius: 50%; padding: 10px; margin-right: 10px; text-align: center;"> OFFICE OF INFORMATION DISCLOSURE NOV 16 2001 PATENT & TRADEMARK OFFICE </div> <div> INFORMATION DISCLOSURE CITATION PTO-1449 </div> </div>				ATTORNEY'S DKT No. 025219-336		APPLICATION No. 09/890,120	
				APPLICANT Pelloie.			
				FILING DATE July 27, 2001		GROUP 2811	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
ED	5,753,955	May 1998	Fechner				
ED	5,808,346	Sept 1998	Ueda				
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
ED	EP 0 616 371	Sept 1994	EP				
ED	WO 96/07205	Mar 1996	PCT				
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
ED	Yannis P. Tsiividis, "Operation and Modeling of the Mos Transistor"						
ED	Jean-Pierre Colinge, "An SOI Voltage-Controlled Bipolar-MOS Device" <i>IEEE Transactions on Electron devices</i> , vol. ED-34, April 1987. Page 845--849.						
ED	Mishel Matloubian, "Analysis of Hybrid-Mode Operation of SOI Mosfets" <i>1993 IEEE</i> . Page 106-107.						
ED	Fariborz Assaderaghi, "A Dynamic Threshold Voltage MOSFET (DTMOS) for Ultra-Low Voltage Operation" <i>1994 IEEE</i> . Page 809-812.						
EXAMINER				DATE CONSIDERED 1/25/03			

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.